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Application Number:	10790578			
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Title of Invention:	ARRAY ARCHITECTURE AND PROCESS FLOW OF NONVOLATILE MEMORY DEVICES FOR MASS STORAGE APPLICATIONS			
First Named Inventor/Applicant Name:	Peter W. Lee			
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File Listin	g:				
Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1 Power of Attorney	POA_DRATS_signed_APlus.pdf	1052351	no	2	
		92x93f18cs07472x9d2d0f1ctfs241998cd6 4bbss			
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2 Assignee showing of ownership per 37 CFR 3.73(b). statement.pdf	Total Files Size (in bytes): 1090607					
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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